



THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICATION OF:

Adolf Bernds et al.

SERIAL NO:

10/569,233

GROUP ART UNIT:

Not

assigned

FILED:

February 22, 2006

EXAMINER:

Not

assigned

FOR:

Polymer Mixtures for Printed Polymer Electronic Circuits

ATTY DKT NO.:

411000-148

CUSTOMER NO.: 27162

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

SIR:

Pursuant to 37 C.F.R. §1.56(a), Applicant(s) hereby cite(s) the enclosed documents listed on the attached copy of Form PTO-1449. Applicant(s) make no admission that the cited references are prior art or that these references are in fact material to the patentability of the above-entitled application.

This Information Disclosure Statement is filed in accordance with the paragraph of 37 CFR §1.97 indicated below:

X §1.97(b)

This Information Disclosure Statement is filed:

- Within three months of the filing date of a national (1) application; OR
- Within three months of the date of entry of the national (2) stage of an international application; OR
- Before the mailing of a first Office Action on the merits. (3) No fee or statement is required.

§1.97(c) This Information Disclosure Statement is filed after the period specified in paragraph (b) above, but before the mailing date of either:

- A Final Action or under 37 CFR §1.113; OR (1)
- A Notice of Allowance under 37 CFR §1.311; AND (2)

is accompanied by either: (check one)

		The statement as specified in 37 CFR §1.97(e) set out below; OR
		The fee of \$180.00 under 37 CFR §1.17(p).
§1.97(d)	This Information	tion Disclosure Statement is filed after the mailing date
	(1)	a Final Action or under 37 CFR §1.113; OR
	(2)	A Notice of Allowance under 37 CFR §1.311;
	` '	or before payment of the Issue Fee; AND
		ompanied by:
	(1)	The statement as specified in 37 CFR §1.97(e) as set forth below; AND
	(2)	Petition is hereby made under 37 CFR §1.97(d) for consideration of this Information Disclosure Statement; AND,
	(3)	The petition fee of \$180.00 set out in 37 CFR §1.17(i).
§1.97(e)	The undersig	ned Attorney hereby states that:
	Statement was	f information contained in this Information Disclosure as first cited in any communication from a foreign patent unterpart foreign application not more than three months ing date of this Information Disclosure Statement;
	no item of Statement wa in a counter undersigned any individua	information contained in this Information Disclosure as cited in a communication from a foreign patent office rpart foreign application, or to the knowledge of the Attorney after making reasonable inquiry, was known to all designated in 37 CFR §1.56(c) more than three r to the filing date of the Information Disclosure

The relevancy of the cited foreign language documents in the attached Form 1449 is that these were cited in a foreign counterpart of this application and/or one or more of the co-pending, commonly owned U.S. applications listed in the attached Disclosure Statement.

The Commissioner is authorized to charge payment of any fees associated with this communication or credit any overpayment to Deposit Account No. 03-0678.

FIRST CLASS CERTIFICATE

I hereby certify that this correspondence is being deposited today with the U.S. Postal Service as First Class Mail in an envelope addressed to:

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Janice Speidel

May 31, 2006

Date

290450v1

Respectfully submitted, Adolf Bernds et al

JUN 0 5 2006

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Approved for use through 07/31/2006. OMB 0651-0031

U.S. Patent and Trademark Office; U.S. Department of Commerce respond to a collection of information unless it displays a valid OMB control number Under the Paperwork Reduction Act Substitute for form 1449A/PTO Application Number 10/569,233 **INFORMATION DISCLOSURE** Feb. 22, 2006 Filing Date STATEMENT BY APPLICANT Bernds First Named Inventor **Group Art Unit** Not assigned (Use as many sheets as necessary) [IV] Not assigned **Examiner Name** 1 Of 13 Attorney Docket Number 411000-148 Sheet

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			Application Number	10/569,233
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			Group Art Unit	Not assigned
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APPLICANTS:

Adolf Bernds et al.

SERIAL NO:

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10/569,233

FILED:

February 22, 2006

EXAMINER

Not assigned

ART UNIT

Not assigned

FOR:

Polymer Mixtures for Printed Polymer Electronic Circuits

ATTY DKT NO.:

411000-148

CUSTOMER NO.:

27162

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

DISCLOSURE STATEMENT UNDER 37 CFR 1.56

SIR:

This paper is to bring to the attention of the PTO the following commonly owned copending U.S. applications, all of which are related in different respects to organic electronic devices and/or method of making such devices such as transistors, diodes, integrated circuits and the like. Many of these applications also have one or more common inventors. The attached PTO 1449 lists these applications. It is respectfully requested that the Examiner consider and make of record all of the cited applications listed on the attached PTO 1449.

Application No.	<u>Title</u>	Inventors	Atty. Dkt. No.
10/344,951	Organic Field-Effect Transistor (OFET), A Production Method Therefor, An Integrated Circuit Constructed From the Same and Their Uses	Adolf Bernds et al.	411000-99
10/362,932	Organic Field Effect Transistor, Method for Structuring an OFET and Integrated Circuit	Adolf Bernds et al	411000-110
10/380,113	Organic Rectifier, Circuit, RFID Tag and Use of an Organic Rectifier	Adolf Bernds et al.	411000-106

10/381,032	Electrode and/or Conductor Track for Organic Components and Production Method Thereof	Adolf Bernds et al.	411000-105
10/433,959	Organic Field Effect Transistor, Method For Structuring an OFET and Integrated Circuit	Adolf Bernds	411000-108
10/433,961	Device For Detecting and/or Transmitting at Least One Environmental Influence, Method for Producing Said Device and Use Thereof	Wolfgang Clemens et al.	411000-111
10/467,636	Organic Field Effect Transistor With a Photostructured Gate Dielectric, Method for the Production and Use Thereof in Organic Electronics	Adolf Bernds et al.	411000-104
10/473,050	Device With At Least Two Organic Electronic Components and Method for Producing the Same	Adolf Bernds et al.	411000-113
10/479,234	Organic Field Effect Transistor, Method for Production and Use Thereof in the Assembly of Integrated Circuits	Adolf Bernds et al.	411000-101
10/479,238	Method For Producing Conductive Structures by Means of Printing Technique, and Active Components Produced Therefrom For Integrated Circuits	Adolf Bernds et al.	411000-100
10/492,922	Insulator for An Organic Electronic Component	Erwann Guillet et al.	411000-115
10/492,923	Electronic Unit, Circuit Design for the Same and Production Method	Wolfgang Clemens et al.	411000-114
10/498,610	Organic Field Effect Transistor with Offset Threshold Voltage and the Use Thereof	Walter Fix et al.	411000-119
10/508,640	Logic Component Comprising Organic Field Effect Transistors	Walter Fix et al.	411000-120
10/508,737	Device and Method for Laser Structuring Functional Polymers and	Adolf Bernds et al.	411000-121
10/517,750	Substrate for an Organic Field Effect Transistor, Use of the Substrate, Method of Increasing the Charge Carrier Mobility and Organic Field Effect Transistor (OFET)	Wolfgang Clemens et al.	411000-122
10/523,216	Electronic Component Comprising Predominantly Organic Functional Materials And A Process For The Production Thereof	Adolf Bernds et al.	411000-123
10/523,487	Electronic Device	Wolfgang Clemens et al.	411000-124
10/524,646	Organic Component for Overvoltage Protection and Associated Circuit	Walter Fix et al.	411000-127
10/533,756	Organic Electronic Component with High- Resolution Structuring and Process for	Wolfgang Clemens et al.	411000-128

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	the Production Thereof		
10/534,678	Measuring Apparatus for Determining an Analyte in a Liquid Sample	Wolfgang Clemens et al.	411000-129
10/535,448	Organic Electronic Component Comprising Semi-Conductive Functional Layer and Method for Producing Said Component	Wolfgang Clemens et al.	411000-131
10/535,449	Organic Electronic Component Comprising the Same Organic Material for at Least Two Functional Layers	Adolf Bernds et al.	411000-132
10/344,926	An Electronic Circuit Having an Encapsulated Organic-Electronic Component, and a Method for Making an Encapsulated Organic-Electronic Component	Wolfgang Clemens et al.	411000-133
10/541,815	Organo-Resistive Memory Unit	Axel Gerlt et al.	411000-136
10/541,956	Board or Substrate for an Organic Electronic Device and Use Thereof	Wolfgang Clemens et al.	411000-137
10/541,957	Organic Field Effect Transistor And Integrated Circuit	Walter Fix et al.	411000-138
10/543,561	Organic Storage Component and Corresponding Triggering Circuit	Wolfgang Clemens et al.	411000-139
10/542,678	Organic Electronic Component and Method For Producing Organic Electronic Devices	Adolf Bernds et al.	411000-140
10/542,679	Use of Conductive Carbon Black/Graphite Mixtures for the Production of Low-Cost Electronics	Adolf Bernds et al.	411000-141
10/562,989	Method and Device for Patterning Organic Layers	Jurgen Ficker	411000-143
10/562,869	Logic Gate With a Potential-Free Gage Electrode for Organic Integrated Circuits	Wolfram Glauert	411000-144
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10/568,730	Organic Capacitor With Voltage- Controlled Capacitance	Wolfgang Clemens	411000-147
10/569,233	Polymer mixtures for printed polymer electronic circuits	Adolf Bernds	411000-148
10/570,571	Mechanical Control Elements For Organic Polymer Electronic Devices	Wolfgang Clemens	411000-149

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Substitute for form 1449A/PTO		Com	Complete if Known	
			Application Number	10/569,233
INFORMATION DISCLOSURE			Filing Date	February 22, 2006
STATEMENT BY APPLICANT		First Named Inventor	Adolf Bernds	
		,	Group Art Unit	Not assigned
(Use as many sheets as necessary)		Examiner Name	Not assigned	
Sheet	1	2	Attorney Docket Number	411000-148

			U.S. PATENT DOC	UMENTS	
Examiner Initial*	Cite No.1	Document Number Number-Kid Code ^{2 (if known)}	Publication- Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	133	US-10/344,926	02/12/2004	Adolf Bernds et al.	See Disclosure Statements filed
	99	US-10/344,951	02/12/2004	Adolf Bernds et al.	
	106	US-10/380,113	09/25/2003	Adolf Bernds et al.	
	105	US-10/381,032	02/12/2004	Adolf Bernds et al.	
	108	US-10/433,959	04/01/2004	Adolf Bernds	
	111	US-10/433,961	04/01/2004	Wolfgang Clemens et al.	
	109	US-10/451,108	05/13/2004	Mark Giles et al.	
	104	US-10/467,636	11/04/2004	Adolf Bernds et al.	
	113	US-10/473,050	05/20/2004	Adolf Bernds et al.	
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	115	US-10/492,922	03/03/2005	Erwann Buillet et al.	
	114	US-10/492,923	12/23/2004	Wolfgang Clemens et al.	
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	123	US-10/523,216	02/02/2006	Adolf Bernds et al.	
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	127	US-10/524,646	05/11/2006	Walter Fix et al.	
	128	US-10/533,756	N/A	Wolfgang Clemens et al.	
	129	US-10/534,678	N/A	Wolfgang Clemens et al.	
	131	US-10/535,448	N/A	W. Clemens et al.	
	132	US-10/535,449	2/16/2006	Walter Fix et al.	

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	(Use as many sheets as nec	essary)	Examiner Name	Not assigned
Sheet.	2	2	Attorney Docket Number	411000-148

. 133	3 US-10/344,926	02/12/2004	Adolf Bernds	et al.
130	S US-10/541,815	N/A	Axel Gerlt et a	al.
13	7 US-10/541,956	N/A	Wolfgang Cle	emens et al.
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148	3 US-10/569,233	N/A	Adolf Bernds et al.	
149	US-10/570,571	N/A	Clemens et al.	
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